**Unit 4.1: Processor Fundamentals (Week 7: Weekly Test )**

1a) Name three buses used in the Von Neumann architecture.

[3 Marks]

1.Address bus,data bus and control bus.

b) Describe the function of each named bus.

[3 marks]

1.The address bus Carrie’s addresses throughout the computer system

2.The data bus is bidirectional.lt allows data to be sent in both directions along the bus.

3.It is usually 8-bits wide since it only Carrie’s control signals.

c) Describe how bus width and clock speed can affect computer [4 marks ]

performance.

1.increasing clock speed usually increases the speed of a computer.

2.use of cache memories can also speed up a processor’s performance

3.changed by altering bus width,clock speed and use of multi-core CPUs